





MDIO\_EN: 0-UART, 1-MDC/MDIO

CTRL\_DAC\_HW1:

PWR\_SEL: 0-1.2VEXT 1-1.2VINT

AZ\_EN: 0-AZ DIS 1-AZ EN

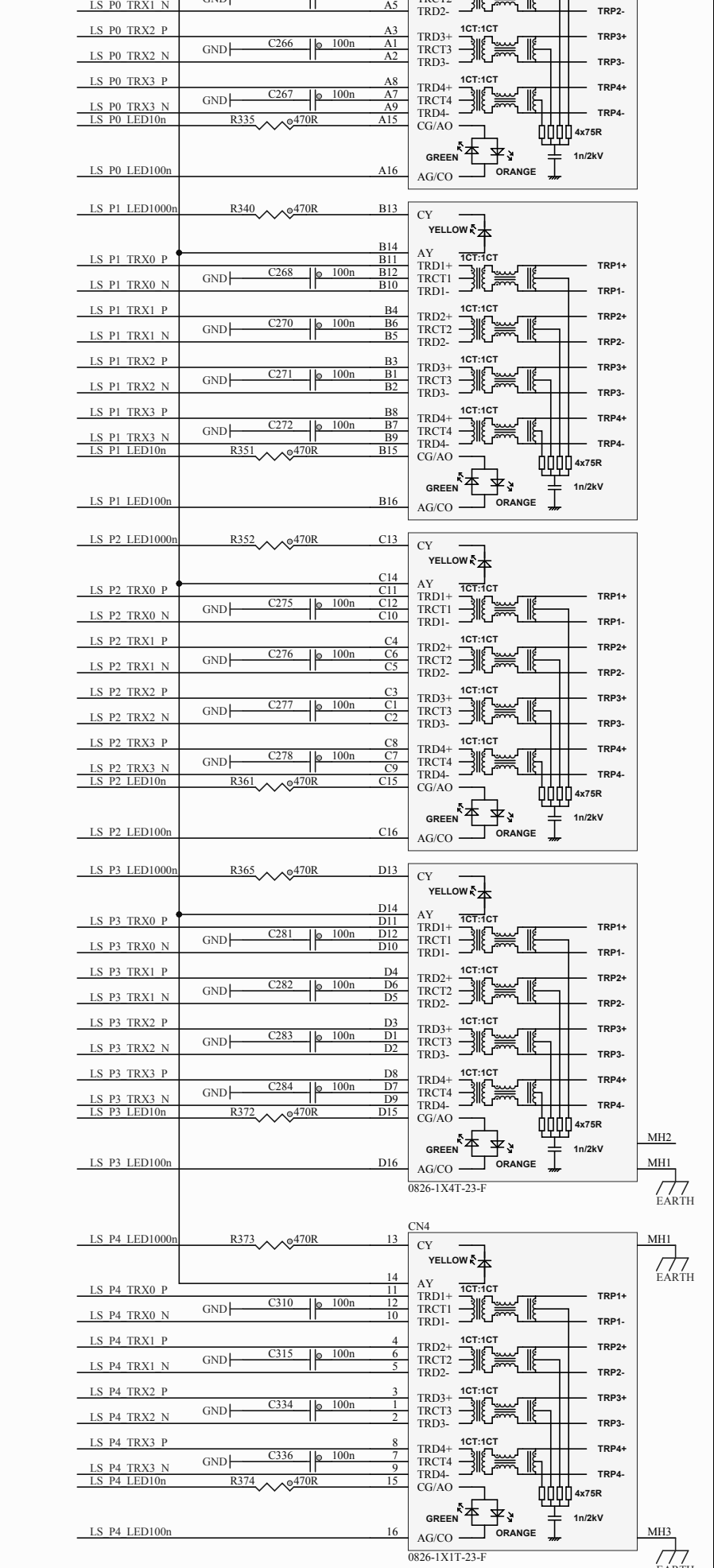
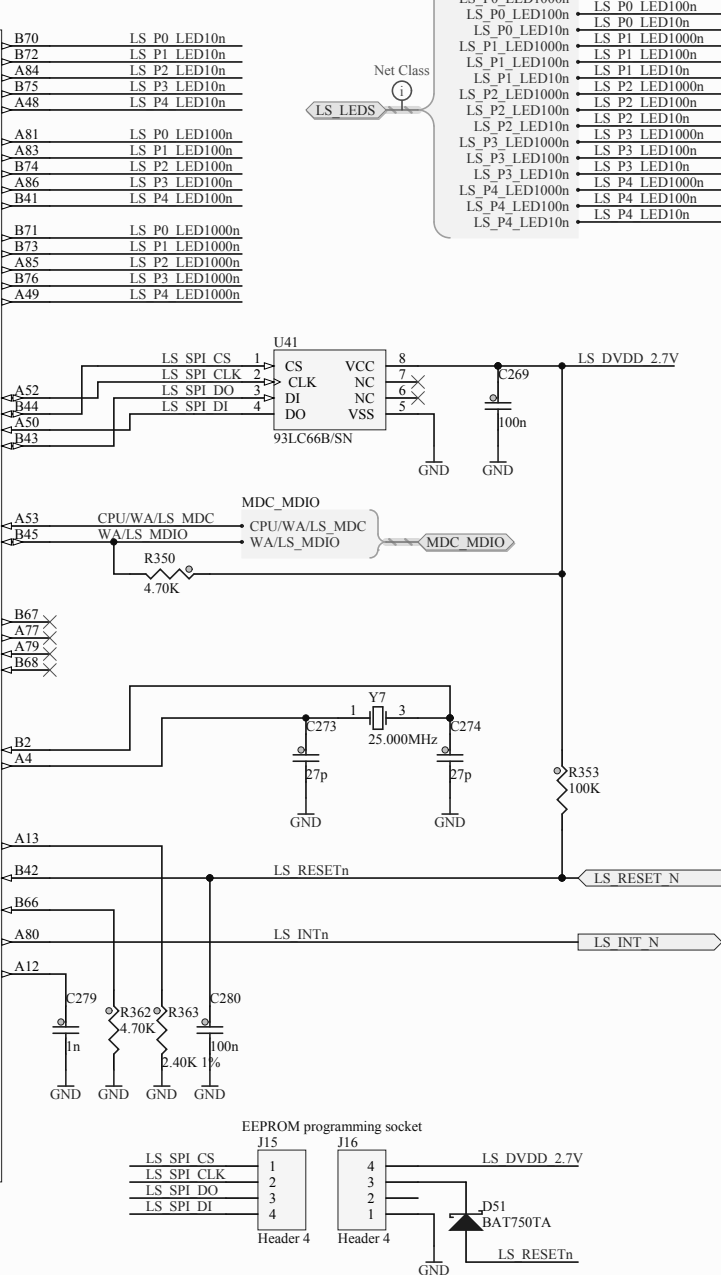
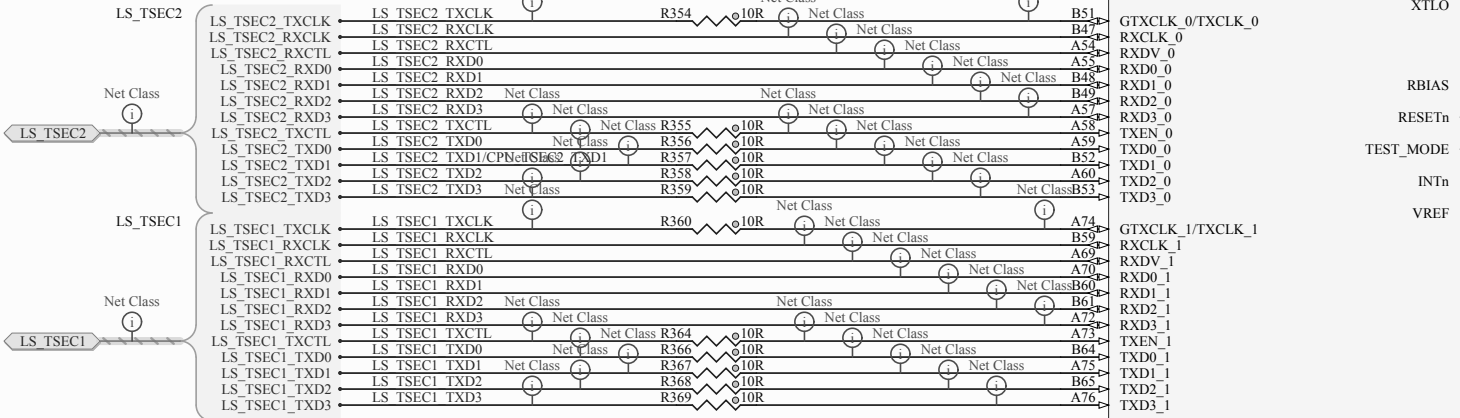
CTRL\_DAC\_HW0:

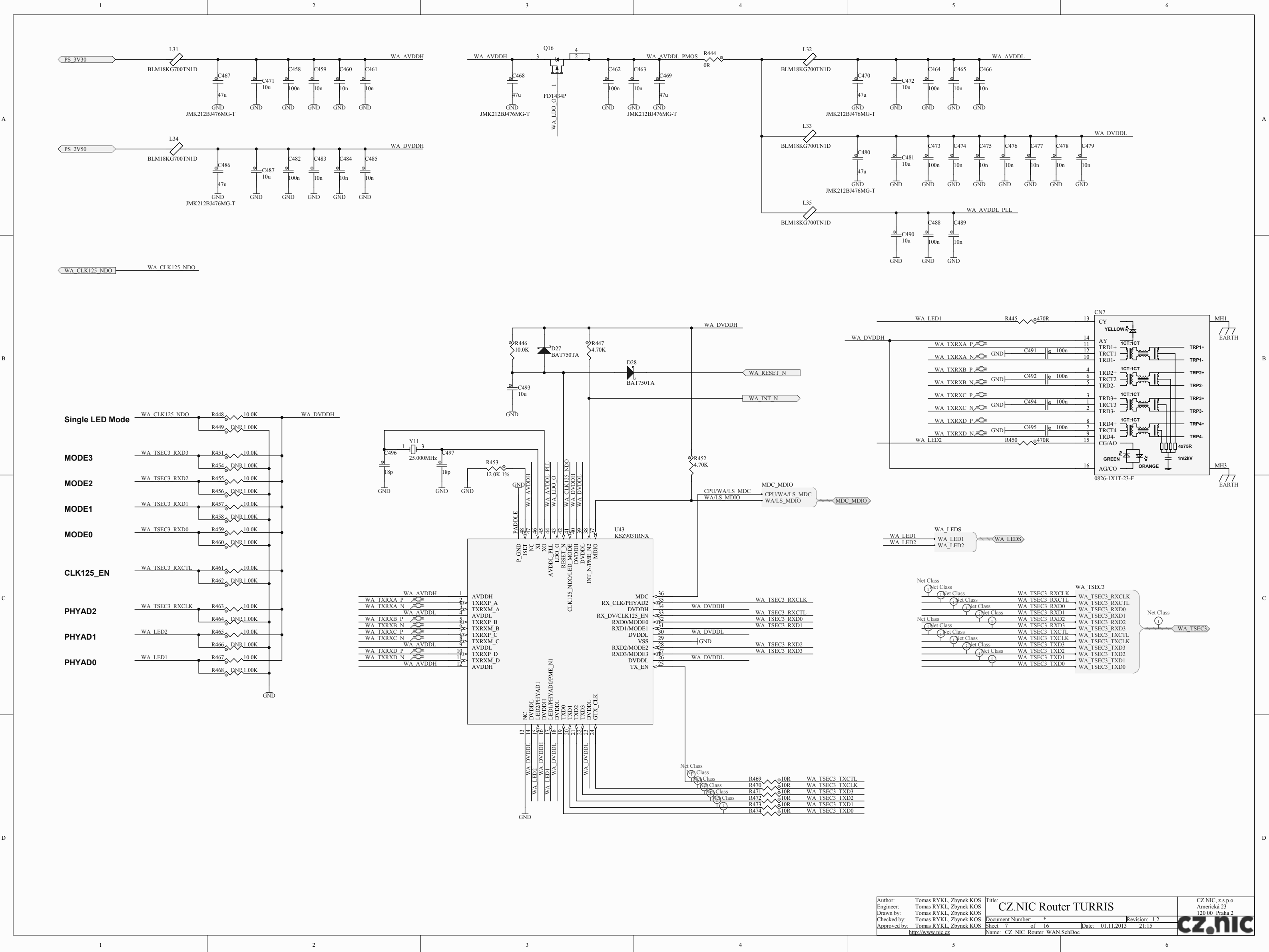
SPI\_EN: 0-NO EEPROM 1-EEPROM

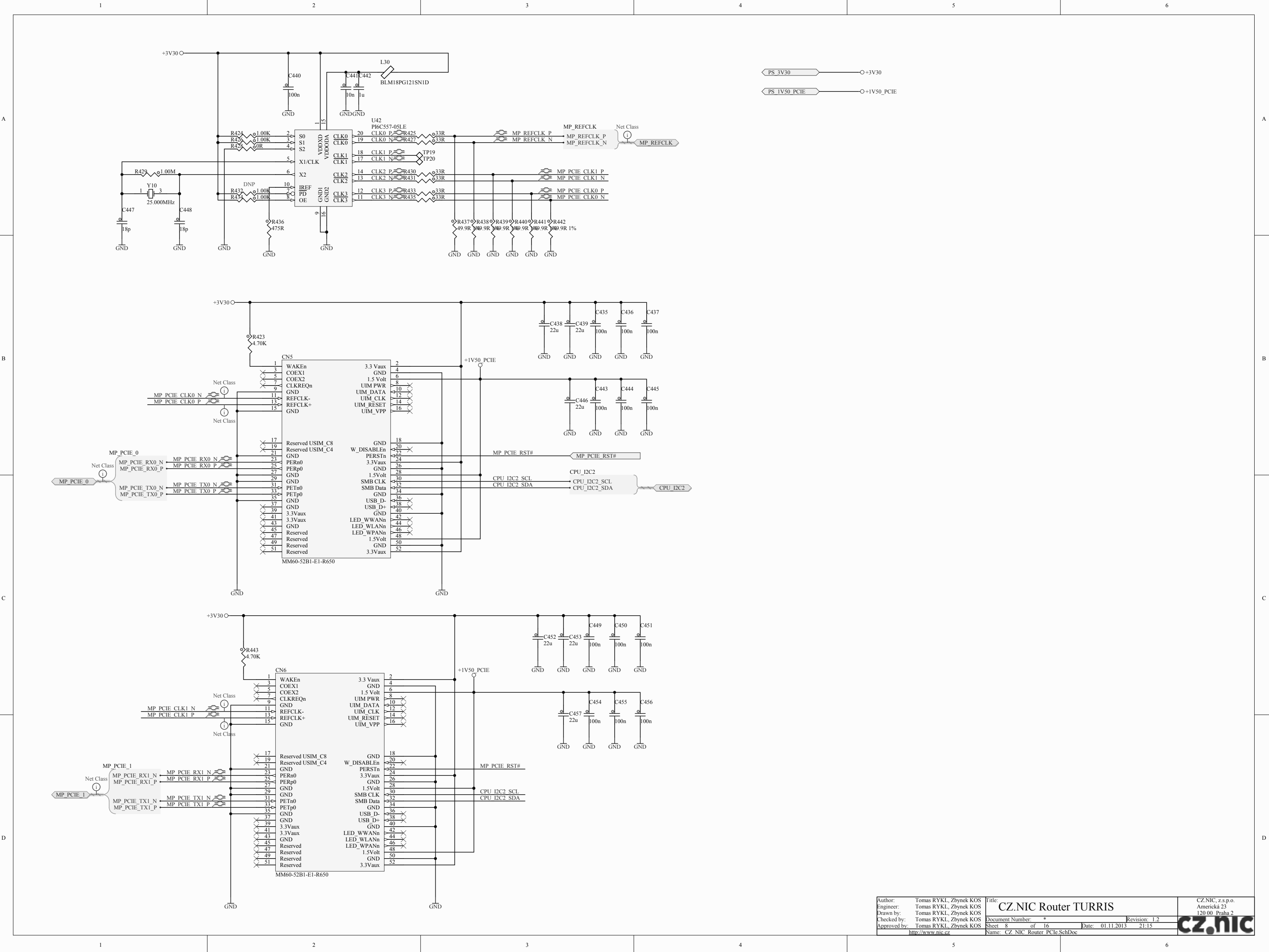
SPI\_SIZE:0-1K 1-2K/4K

LED\_OPEN\_EN:0-DRIVER 1-OPENDRAIN

CABLE\_DIAG:0-DISABLE 1-ENABLE



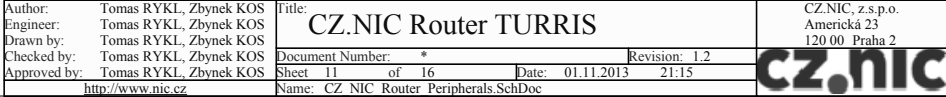


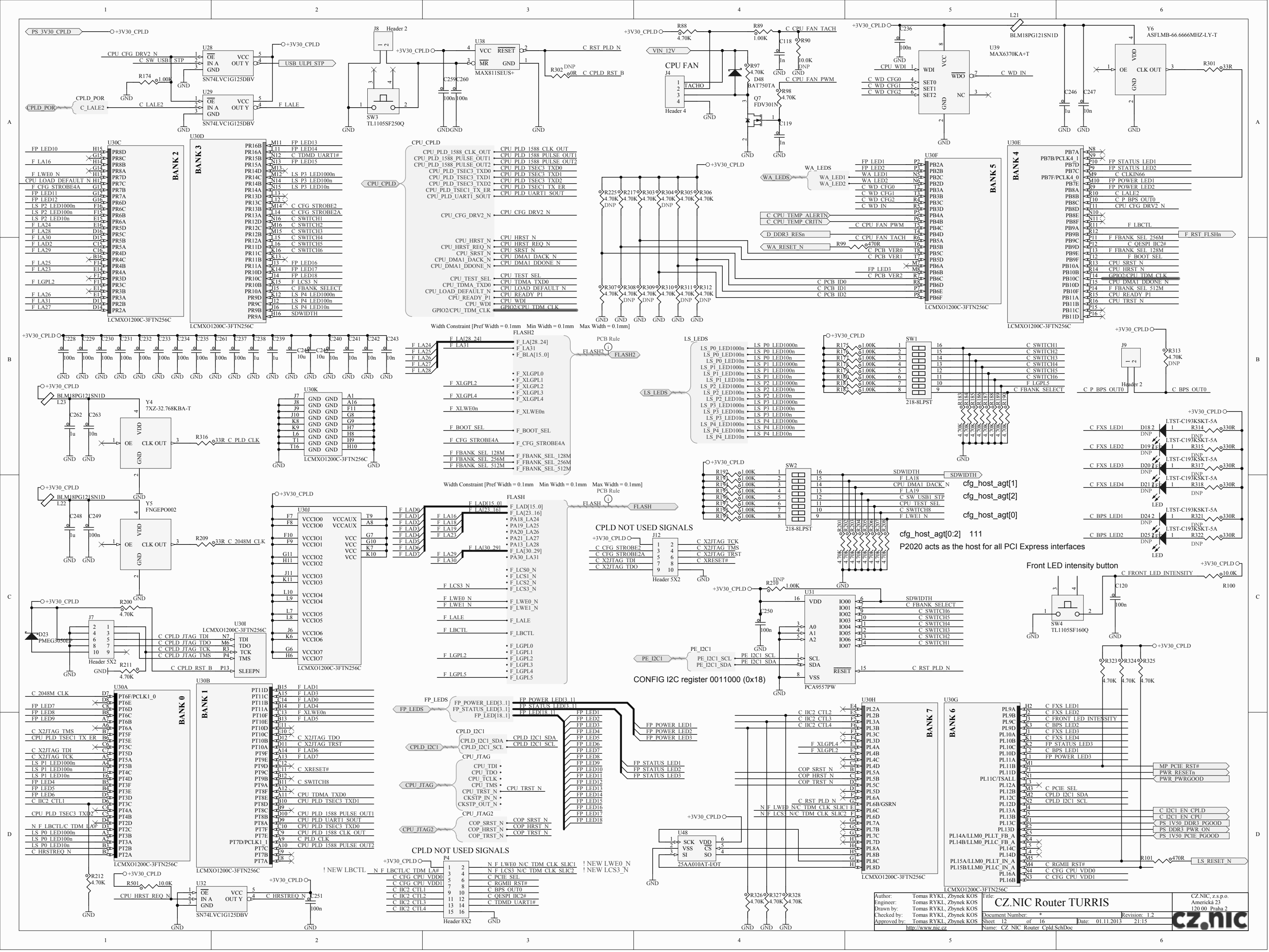














A

B

C

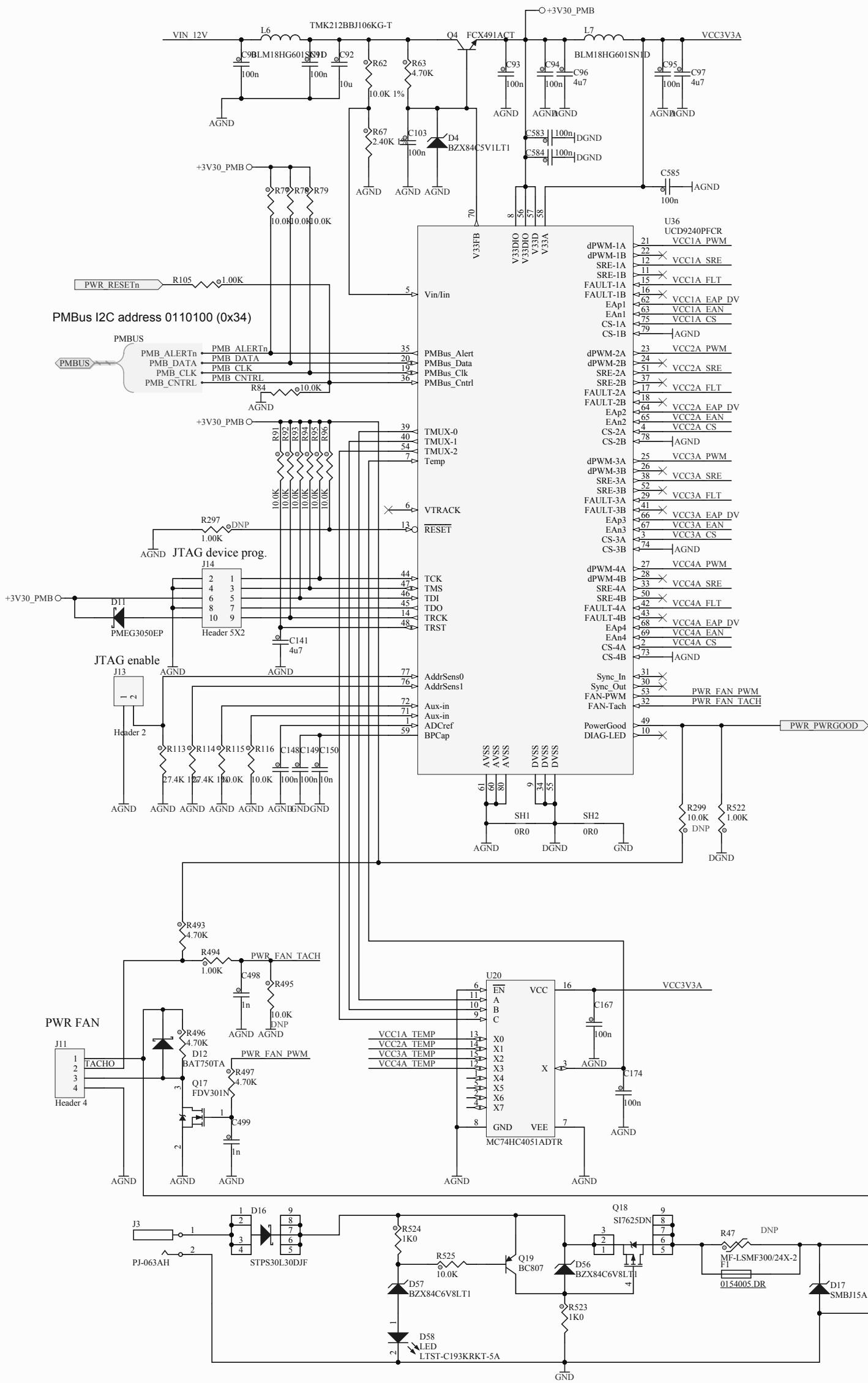
D

A

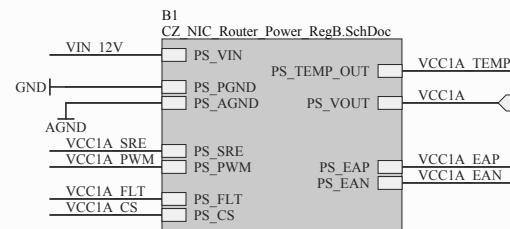
B

C

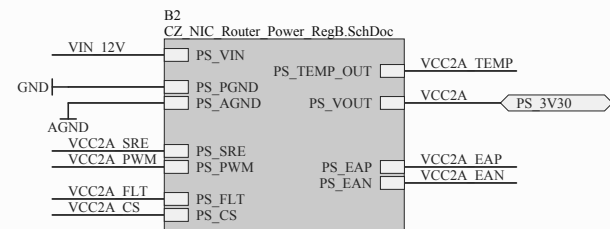
D



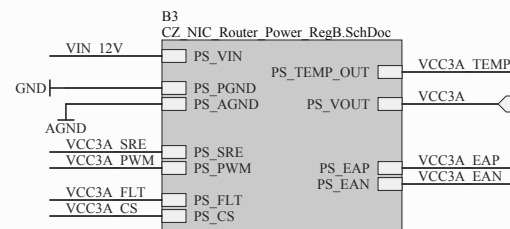
## 1.05V/10A CORE



## 3.30V/10A ALL, PCIe



## 5.00V/7A SUBREGs, USB



## 2.50V/5A

